

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	565	"725"/\$.cccls. and (((hdd hard adj disk) and (RAM)) same (processor\$1 cpu microprocessor\$1))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 10:23
L3	198	"725"/\$.cccls. and (((hdd hard adj disk) and (RAM)) same (processor\$1 cpu microprocessor\$1)) and (temporal\$4 temporar\$5) same (stor\$4 buffer\$3 cach\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 10:46
L4	143	"725"/\$.cccls. and (((hdd hard adj disk) and (RAM)) same (processor\$1 cpu microprocessor\$1)) and (immediat\$4 now) same (stor\$4 buffer\$3 cach\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 11:11
L5	477	"725"/\$.cccls. and (((hdd hard adj disk) and (RAM)) same (processor\$1 cpu microprocessor\$1)) and (tim\$3 same (stor\$3 cach\$3))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:44
L6	1	"20020188956".did. and application\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 12:56
L7	1	"20020016963".did. and RAM	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:37
L8	1	"20020016963".did. and "105"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:37
L9	34	inoue-hideki\$4	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:39
L10	2	inoue-hideki\$4 and (ram and (hdd hard adj disk)) and (cpu processor microprocessor)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:42
L11	2	inoue-hideki\$4 and (ram and (hdd hard adj disk)) and (cpu processor microprocessor) and (tim\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:42
L13	118	"725"/68,131,134,139,142,151,110. cccls. and (((hdd hard adj disk) and (RAM)) same (processor\$1 cpu microprocessor\$1)) and (tim\$3 same (stor\$3 cach\$3))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:51

L14	120	"725"/68,131,134,139,142,151,110. ccls. and (((hdd hard adj disk) and (RAM physical adj memory main adj memory)) same (processor\$1 cpu microprocessor\$1)) and (tim\$3 same (stor\$3 cach\$3))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 13:52
L15	28	"725"/68,131,134,139,142,151,110. ccls. and (((hdd hard adj disk) and (RAM physical adj memory main adj memory)) same (processor\$1 cpu microprocessor\$1)) and (tim\$3 same (stor\$3 cach\$3)) same (remov\$3 discard\$3 delet\$5)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 14:02
L16	22	"725"/68,131,134,139,142,151,110. ccls. and (((hdd hard adj disk) and (RAM physical adj memory main adj memory)) same (processor\$1 cpu microprocessor\$1)) and (tim\$3 same (stor\$3 cach\$3)) same (epg pg program\$5 adj guide)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 14:08
L17	343	(tim\$3 with compar\$5) and (tim\$3 same (remov\$3 discard\$3 delet\$4) same (stor\$3 memory hard adj disk hdd ram)) and "725"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 14:11
L18	73	(tim\$3 with compar\$5) and (tim\$3 same (remov\$3 discard\$3 delet\$4) same (stor\$3 memory hard adj disk hdd ram)) and "725"/68,131,134, 139,142,151,110.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 14:36
L19	22	(tim\$3 with compar\$5) and (tim\$3 same (remov\$3 discard\$3 delet\$4) same (stor\$3 memory hard adj disk hdd ram physical adj memory main adj memory)) and (cpu processor microprocessor) and (hdd hard adj disk) and (physical adj memory main adj memory memory) and "725"/68,131,134,139,142,151,110. ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 14:55
L20	23	(tim\$3 with compar\$5) and (tim\$3 same (remov\$3 discard\$3 delet\$4) same (stor\$3 memory hard adj disk hdd ram physical adj memory main adj memory)) and (cpu processor microprocessor) and (hdd hard adj dis\$1) and (physical adj memory main adj memory memory) and "725"/68,131,134,139,142,151,110. ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:34
L21	1	"5652613".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:35

L22	1734	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) and "725"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:35
L23	1240	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) and (epg ipg pg program\$5 adj guide) and "725"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:37
L24	521	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) and (epg ipg pg program\$5 adj guide) and (hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory) and (cpu processor microprocessor) and "725"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:38
L25	32	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) with (epg ipg pg program\$5 adj guide) and (hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory) and (cpu processor microprocessor) and "725"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:41
L26	17	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) with (epg ipg pg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) same(cpu processor microprocessor) and "725"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:49
L27	1	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) with (epg ipg pg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) same(cpu processor microprocessor) and "725"/50.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:49
L28	3	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) with (epg ipg pg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) and (cpu processor microprocessor) and "725"/50.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:51

L29	12	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) and (epg ipg pg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) and (cpu processor microprocessor) and "725"/50.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 16:40
L30	4	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) with (schedule adj (information data) epg ipg pg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) and (cpu processor microprocessor) and "725"/50.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:52
L31	4	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4) with (schedule\$1 epg ipg pg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) and (cpu processor microprocessor) and "725"/50.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:53
L32	8	(memor\$3 storage\$1 cache\$1) with (updat\$3) with (schedule\$1 epg ipg pg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) and (cpu processor microprocessor) and "725"/50.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 15:53
L33	378	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4 updat\$3) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) and (cpu processor microprocessor) and "725"/39-61.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 16:40
L34	176	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4 updat\$3) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) same (cpu processor microprocessor) and "725"/39-61.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 16:41

L35	113	(memor\$3 storage\$1 cache\$1) near4 (exchang\$3 swap\$4 manage\$4 updat\$3) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) same (cpu processor microprocessor) and "725"/39-61. ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 16:41
L36	29	(memor\$3 storage\$1 cache\$1) near4 (exchang\$3 swap\$4 manage\$4 updat\$3) same (schedul\$3 epg pg ipg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) same (cpu processor microprocessor) and "725"/39-61. ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:13
L37	349	bhatt.in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 16:55
L38	7	bhatt-bhaves\$5.in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 16:56
L39	14	(initial\$5) same stor\$4 same (hard adj (disk\$2 drive) hdd) and (stor\$3 same (ram physical adj memory main adj memory)) and 725/39-51. ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:11
L40	1	"6993789"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:14
L41	23	(memor\$3 storage\$1 cache\$1) near4 (exchang\$3 swap\$4 manage\$4 updat\$3) same (schedul\$3 epg pg ipg program\$5 adj guide) and ((hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory)) same (cpu processor microprocessor) and "725"/68,131, 134,139,142,151,110.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:13

L42	23	(memor\$3 storage\$1 cache\$1) near4 (exchang\$3 swap\$4 manage\$4 updat\$3) same (schedul\$3 epg pg ipg program\$5 adj guide) and ((long adj term adj (memory storage cache) hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory short adj term adj (memory storage cache))) same (cpu processor microprocessor) and "725"/68,131,134,139,142,151,110.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:23
L43	54	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4 updat\$3) same (schedul\$3 epg pg ipg program\$5 adj guide) and ((long adj term adj (memory storage cache) hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory short adj term adj (memory storage cache))) and (cpu processor microprocessor) and "725"/68,131,134,139,142,151,110.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:29
L44	31	l43 not l42	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:24
L45	6	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4 updat\$3) same (schedul\$3 epg pg ipg program\$5 adj guide) and ((long adj term adj (memory storage cache) hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory short adj term adj (memory storage cache))) and (cpu processor microprocessor) and "725"/38.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:30
L46	163	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4 updat\$3) same (schedul\$3 epg pg ipg program\$5 adj guide) and ((long adj term adj (memory storage cache) hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory short adj term adj (memory storage cache))) and (cpu processor microprocessor) and "725"/39-61.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:30

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L47	56	(memor\$3 storage\$1 cache\$1) with (exchang\$3 swap\$4 manage\$4 updat\$3) same (schedul\$3 epg pg ipg program\$5 adj guide) and ((long adj term adj (memory storage cache) hard adj dis\$1 hdd storage) and (ram main adj memory physical adj memory short adj term adj (memory storage cache))) same (cpu processor microprocessor) and "725"/39-61.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:41
L48	221	(memor\$3 storage\$1 cache\$1) with (schedul\$3 epg pg ipg program\$5 adj guide) same ((long adj term adj (memory storage cache) hard adj dis\$1 hdd storage)) and ((ram main adj memory physical adj memory short adj term adj (memory storage cache))) same (cpu processor microprocessor) and "725"/39-61.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:42
L49	70	(memor\$3 storage\$1 cache\$1) with (schedul\$3 epg pg ipg program\$5 adj guide) same ((long adj term adj (memory storage cache) hard adj dis\$1 hdd storage)) and ((ram main adj memory physical adj memory short adj term adj (memory storage cache))) same (cpu processor microprocessor) and "725"/68,131,134,139,142,151,110.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/09 17:42